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** CONTINUING DATA ***** AC

This application is a CON of 10/355,301 01/31/2003 PAT 6,784,116 AC
 which is a CON of 10/013,454 12/13/2001 PAT 6,528,403 AC
 which is a CON of 09/773,000 01/31/2001 PAT 6,503,819 AC
 which is a CON of 09/086,568 05/29/1998 PAT 6,197,702 AC

** FOREIGN APPLICATIONS ***** AC

JAPAN 9-142315 05/30/1997 AC

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i>	INITIALS <i>[Initials]</i>		
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TITLE
 Fabrication process of a semiconductor integrated circuit device